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nct 0 5 2000 E		Docket No. 0756-1984
IN THE UNITED STATES PATE	NT AND	TRADEMARK OFFICE #10/Electron
In re Patent Application of	)	Amil C
Shunpei YAMAZAKI	)	Group Art Unit: 2811 المدراها
Serial No. 09/334,646	)	Examiner: S. HU
Filed: June 17, 1999	)	CERTIFICATE OF MAILING
riied. Julie 17, 1999	,	I hereby certify that this
For: SEMICONDUCTOR DEVICE AND	)	consciondence to being deposited with the United States Postal Service with
FABRICATION METHOD THEREOF	)	cufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 2023, on 10-4-0
AMENDMENT AND RESPONSE	TO ELE	(1)(2)
		RECEIVED
Assistant Commissioner for Patents Washington, D.C. 20231		
Washington, D.C. 20231		UCI 19 2000

In response to the Office Action of August 4, 2000, please amend the subject application as follows:

TECHNOLOGY CENTER 2800

## IN THE CLAIMS:

Sir:

Cancel claims 4, 5, 7, 9, 10, 20, 22-25, 29-31, 35-37, 44-47, 50, 51, 54-57, 61, 62, 64, and 66-70 without prejudice to applicant('s') right to file a divisional application with respect thereto.

## Please amend the following claims:

1. (Twice Amended) An electro-optical device comprising:

at least two transistors provided on an insulating surface in a XY-branching circuit of a [peripheral] <u>driver</u> circuit of said electro-optical device;

a common gate wiring provided on said insulating surface and connected with said two transistors at gate electrodes of said two transistors;

a common source wiring provided on said insulating surface and connected with said two